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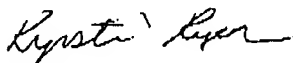
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Date: July 10, 2007TO: Commissioner for Patents
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<u>U.S. Serial No. or Patent No.</u>	<u>Our Ref No.</u>	<u>U.S. Serial No. or Patent No.</u>	<u>Our Ref No.</u>
10/917,728	P19595	10/779,204	P18228
10/839,529	P18325	10/863,664	P18326
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I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below.



Kyrstin Ryan

July 10, 2007

Date of Transmission

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S/N 09/990,754PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mark T. Feuerstraeter et al. Examiner: Ajit Patel
Serial No.: 09/990,754 Group Art Unit: 2616
Filed: November 16, 2001 Docket: P11858
Title: INTERFACE AND RELATED METHODS FOR RATE PACING IN AN
ETHERNET ARCHITECTURE
Assignee: Intel Corporation

REVOCATION AND POWER OF ATTORNEY
CERTIFICATE UNDER 37 CFR § 3.73(b)

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with 37 C.F.R. Section 1.36, M.P.E.P. Section 402.05 and 402.07, please
revoke any existing Powers of Attorney, if any, and appoint the following attorneys and/or patent
agents to prosecute this application and to transact all business in the Patent and Trademark
Office in connection therewith

CUSTOMER NUMBER: 50890

STATEMENT UNDER 37 CFR § 3.73(b)

Intel Corporation is the assignee of the entire right, title and interest in the patent
application identified above by virtue of an assignment from the inventor filed November 16,
2001 and recorded on Reel 012323, Frames 0256 - 0260.

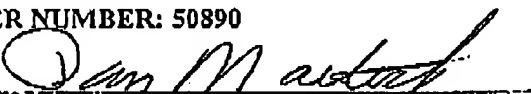
Pursuant to 37 C.F.R. §3.73(b) I hereby declare that I, Pam J. Matlock, am empowered to
sign this statement on behalf of Intel Corporation, the assignee.

Please direct all correspondence in this case to:

Caven & Aghevi, LLC
c/o Intelivate
CUSTOMER NUMBER: 50890

Date 7/9/07

By


Pam J. Matlock
Intel Corporation